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This paper is an introduction to the use of a structured specification method for VLSI design. pag. 4.136-4.138 COMP EURO 89: "VLSI and Computer Peripherals

### **Patent us7010771 - method and apparatus for**

of searching for a global path Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks. Proceedings, May 8-12, 1989,

### **Ieee xplore - conference table of contents**

their Interconnection Networks', Proceedings Proceedings. VLSI and Computer Peripherals. VLSI and Microelectronic Applications in Intelligent Peripherals and

### **Us9075605 - methods and devices for treating**

or PAE groups is limited by their function and in particular by their interconnection. 8 2 : Prime Computer 12 12 : 1989 7

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### **Patent us7351340 - methods for providing kidney**

A number of improvements relating to methods and apparatuses for kidney dialysis are disclosed. These include a method providing an apparatus for dialyzing to a

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Press, Proceedings on VLSI and Computer Peripherals: VLSI and Microelectronic Applications in Intelligent Peripherals their Interconnection Networks, May 8, 1989,

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## **References - springer**

References [1] Apiki, S., and S. Diehl. 1989. "Upscale Monitors." Byte, March, pp. 162-174. [2] PEURO'89: VLSI and Computer Peripherals, pp. 2-(15-19).

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Silicon Auditory Processors as Computer Peripherals 823 4. SYSTEMS IMPLEMENTATION Figure 1 is a block diag'am of this system. A single VLSI chip computes the auditory

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VLSI and computer peripherals : VLSI and microelectronic applications in intelligent peripherals and their interconnection networks, Hamburg, May 8-12, 1989 : proceedings

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IEEE Proceedings: VLSI and Computer Peripherals, VLSI and Microelectronic Applications in Intelligent and their Interconnection Networks, May 1989,

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of the vibrotactile cue may lead to a peripherals, VLSI and microelectronic applications in intelligent peripherals and their interconnection networks

### **Methods for providing kidney dialysis equipment**

interconnection computer system used in the preferred embodiment.FIG. 8 shows a touch screen display used in the preferred embodiment.FIG. 9 shows the touch

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Title: Achieving Byzantine agreement in a generalized network model - CompEuro '89., 'VLSI and Computer Peripherals. VLSI and Microelectronic Applicati ons in Intelligen

### **Design of integrated systems - springer**

Design of Integrated Systems Detlef Schmid Logic Synthesis for VLSI.Proc. CompEuro 89: VLSI and Computer Peripherals, IEEE Computer Society, 1989 /WeKu 88/.

### **Patent us7003752 - method and apparatus for**

VLSI and Computer Peripherals, VSLI and Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks, Proceedings, May 8-12, 1989,

### **Citeseerx silicon auditory processors as**

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