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And Their Interconnection Networks, Hamburg, May  
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Silicon Auditory Processors as Computer Peripherals 823 4. SYSTEMS IMPLEMENTATION Figure 1 is a block diagram of this system. A single VLSI chip computes the auditory

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A number of improvements relating to methods and apparatuses for kidney dialysis are disclosed. These include a method providing an apparatus for dialyzing to a

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Data networks often are classified by their size and may connect to a port 8 on a by the use of Transputer very large scale integration (VLSI)

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### **Us9075605 - methods and devices for treating**

or PAE groups is limited by their function and in particular by their interconnection. 8 2 : Prime Computer 12 12 : 1989 7

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The possibility of achieving better price performance by sweeping more and more functions into a homogeneous silicon VLSI environment stops at the CPU/peripheral

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1989, VLSI and Computer Peripherals, VLSI and Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks, 8-12 May 1989, Hamburg,

### **References - springer**

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### **Methods for providing kidney dialysis equipment**

interconnection computer system used in the preferred embodiment.FIG. 8 shows a touch screen display used in the preferred embodiment.FIG. 9 shows the touch

### **Patent us7003752 - method and apparatus for**

VLSI and Computer Peripherals, VLSI and Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks, Proceedings, May 8-12, 1989,

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Title: Achieving Byzantine agreement in a generalized network model - CompEuro '89., 'VLSI and Computer Peripherals. VLSI and Microelectronic Applications in Intelligen

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Design of Integrated Systems Detlef Schmid Logic Synthesis for VLSI.Proc. CompEuro 89: VLSI and Computer Peripherals, IEEE Computer Society, 1989 /WeKu 88/.

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